



Global Communication Semiconductors, LLC

Corporate & Foundry
23155 Kashiwa Court, Torrance, CA 90505
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Website: www.gcsincorp.com

Process Integration Engineer

Job Req. #0716
Department: RF and Power Technology
Status: Full-Time / Salaried
Shift: Day

Job Description

- Responsible for sub-micro GaN HEMT development, including performance, reliability and yield improvement.
- Will work with III-V diode and bipolar devices in development and process improvement.
- Responsible for photolithography, thin film deposition and wet/dry etch, device characterization.

Job Requirements

- MS in Electrical Engineering or equivalent semiconductor field required, Ph.D. preferred
- 4+ years of professional engineering
- Experience in III-V compound semiconductor devices physics, TCAD simulation, gds layout, wafer FAB processing, device testing, and some project management experience
- Excellent written and verbal communication skills